



PATENT APPLICATION  
Docket No. 4591-342  
Client No. ID12015-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Kyu-Chan LEE, et al. Conf. No. 4320  
Serial No.: 10/621,165 Examiner: Not yet assigned  
Filing Date: July 15, 2003 Group Art Unit: ~~205~~ 2827  
Title: SEMICONDUCTOR MEMORY DEVICE HAVING AN INTERNAL VOLTAGE GENERATION CIRCUIT FOR SELECTIVELY GENERATING AN INTERNAL VOLTAGE ACCORDING TO AN EXTERNAL VOLTAGE LEVEL

INFORMATION DISCLOSURE CITATION  
FORM PTO-1449 (Modified)

FOREIGN PATENT DOCUMENTS

VIET Q. NGUYEN  
PRIMARY EXAMINER

Exam Init <u>VN</u>	Ref <u>      </u>	Document Number 11-086544	Publication Date March 30, 1999	Country Japan	Name Yamazaki Kyoji
---------------------------	----------------------	---------------------------------	---------------------------------------	------------------	------------------------

OTHER DOCUMENTS

Exam Init <u>VN</u>	Ref <u>      </u>	Author, Title, Date, Pertinent Pages, Etc. English Language Abstract of Japanese Publication No. 11-086544
---------------------------	----------------------	---

Examiner: V. Nguyen

VIET Q. NGUYEN  
PRIMARY EXAMINER

Date Considered: 3/25/05